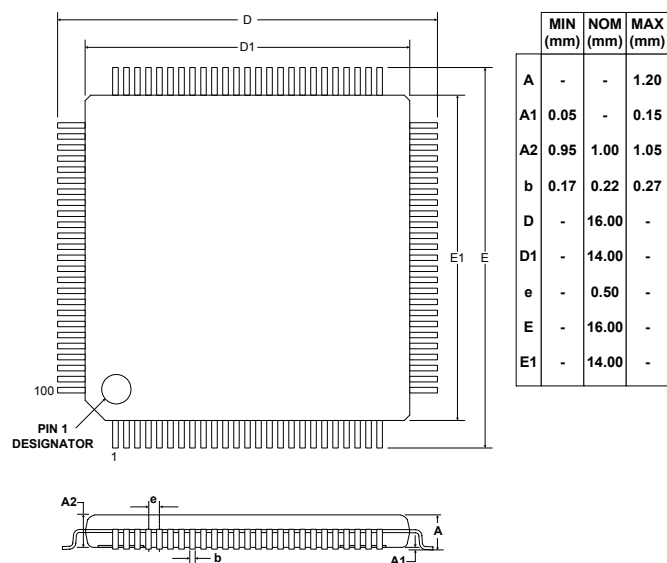




**SELECTED ELECTRICAL SPECIFICATIONS** TA = -40°C to +85°C unless otherwise specified.

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
<b>GLOBAL CHARACTERISTICS</b>					
Supply Voltage		2.7		3.6	V
Supply Current (CPU active)	Clock=50MHz		25		mA
	Clock=1MHz		0.5		mA
	Clock=32kHz		16		μA
Supply Current (shutdown)	Oscillator off; VDD Monitor Enabled		10		μA
	Oscillator off; VDD Monitor Disabled		<0.1		μA
Clock Frequency Range		DC		50	MHz
<b>INTERNAL CLOCKS</b>					
Oscillator Frequency		24.0	24.5	25.0	MHz
PLL Frequency		48	49	50	MHz
<b>A/D CONVERTER</b>					
Resolution			10		bits
Integral Nonlinearity				± 1	LSB
Differential Nonlinearity	Guaranteed Monotonic			± 1	LSB
Signal-to-Noise Plus Distortion		59	61		dB
Throughput Rate				100	ksps
<b>D/A CONVERTERS</b>					
Resolution			12		bits
Differential Nonlinearity	Guaranteed Monotonic			± 1	LSB
Output Settling Time			10		μS
<b>COMPARATORS</b>					
Supply Current	(each Comparator)		1.5		μA
Response Time	(CP+) – (CP-) = 100mV		4.0		μS

**PACKAGE INFORMATION**



**C8051F124DK DEVELOPMENT KIT**

