



Audio control microcontroller containing High-Speed USB host controller and SD host controller, enabling rapid database construction

Features

TOSHIBA

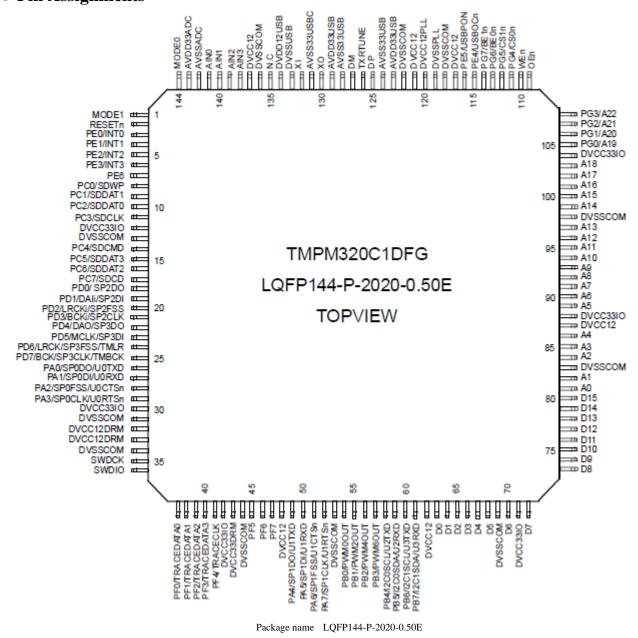
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 ARM CortexTM-M3 Core Operating voltage: Internal= 1.1 to 1.3 V I/O= 3.0 to 3.6 V Maximum Operating frequency: 144 MHz (12 MHz×12 by PLL) Minimum instruction execution time 6.9 ns at 144 MHz operation Built-in Functions USB (high-speed) host controller SD host controller DMA controller Memory controller 	-	1 channel 1 channel 8 channels 2 channels		12MHz PLL WDT I6bit TIMER (8ch) USB Host SD Host	3.3 F ^H SRAM eDRAM eDRAM ARM Cortex -M3 CORE		G→D ^{SCLK/DO/DI} G→DTXD/RXD G→DSDA/SCL
·10-bit AD converter ·16-bit timer/PWM	:	4 channels 8 channels 4 channels	• Memory S	T		_	*Under development
·SSP (SPI/MicroWire mode) ·UART	:	4 channels	Part number TMPM320C1DFG**	·	SRAN 320 Kby	-	eDRAM 1 Mbyte
·I ² C ·Debug circuit (SWD) ·Trace function (ETM)	:	2 channels 1 channel 1 channel	 * SSP: Synchronous Serial Port * SWD: Serial Wire Debug (ARM standard 2-pin interface) * ETM: Embedded Trace Macrocell 				

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 > Contact the Toshiba sales representative for information about RoHS compliance before you purchase any components.

Package Information

Pin Assignments



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